

# **Current Transducer LTSR 6-NP**

For the electronic measurement of currents: DC, AC, pulsed, mixed, with galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).











# $I_{PN} = 6 At$

### Electrical data

I <sub>PN</sub>	Primary nominal current rms	6	At
PN PM	Primary current, measuring range	0 ± 19.2 <sup>1)</sup>	
Î <sub>P</sub>	Overload capability	250	At
<b>V</b> <sub>OUT</sub>	Output voltage (Analog) @ I <sub>D</sub>	2.5 ± (0.625·	I <sub>D</sub> /I <sub>DN</sub> ) V
001	$I_p = 0$	2.5 2)	V
$\mathbf{V}_{REF}$	Reference voltage (internal reference), Ref <sub>OUT</sub> mode	2.5 3)	V
	Reference voltage (external reference), Ref <sub>IN</sub> mode	1.9 2.7 4)	V
G	Sensitivity	104.16	mV/A
N <sub>s</sub>	Number of secondary turns (± 0.1 %)	2000	
R	Load resistance	$\geq 2$	$k\Omega$
C_max	Maximum capacitive loading	500	pF
$R_{\text{IM}}$	Internal measuring resistance (± 0.5 %)	208.33	Ω
TCR	Temperature coefficient of R <sub>IM</sub>	< 50	ppm/K
$V_{\rm c}$	Supply voltage (± 5 %)	5	V
I <sub>C</sub>	Current consumption @ $V_c = 5 \text{ V}$ Typ	$28+I_{S}^{5)}+(V_{OU}$	<b>R</b> /,) mA

# **Accuracy - Dynamic performance data**

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X	Accuracy @ $I_{PN}$ , $T_A = 25^{\circ}C$		± 0.2	%
	Accuracy with $\mathbf{R}_{\text{IM}} \otimes \mathbf{I}_{\text{PN}}$ , $\mathbf{T}_{\text{A}} = 25^{\circ}\text{C}$		± 0.7	%
$\varepsilon_{_{\rm I}}$	Linearity error		< 0.1	%
_			Max	
TCV	Temperature coefficient of $V_{OUT}/V_{RFF} @ I$	<sub>P</sub> = 0		
	- 40°C	+ 85°C	150	ppm/K
TCG	Temperature coefficient of <b>G</b> - 40°C	+ 85°C	50 <sup>6)</sup>	ppm/K
$V_{\text{OM}}$	Magnetic offset voltage @ $I_P = 0$ ,			
	after an overload	of $3 \times I_{PN}$	± 7	mV
		5 x I <sub>PN</sub>	± 8	mV
		10 x <b>I</b> <sub>PN</sub>	± 10	mV
TCV <sub>REF</sub> Temperature coefficient of internal V <sub>REF</sub>				
		+ 85°C	50	ppm/K
	- 40°C	10°C	100	ppm/K
$\mathbf{t}_{ra}$	Reaction time @ 10 % of I <sub>PN</sub>		< 100	ns
$\mathbf{t}_{_{\mathrm{r}}}$	Response time to 90 % of I <sub>PN</sub> step		< 400	ns
di/dt	di/dt accurately followed		> 15	A/µs
BW	Frequency bandwidth (0 0.5 dB)		DC 100	kHz
	(- 0.5 1 dB)		DC 200	kHz

Notes: <sup>1)</sup> Only in ref<sub>out</sub> mode or with external REF less than 2.525 V and greater than 2.475 V. For external REF out of these limits see leaflet. <sup>2)</sup>  $\mathbf{V}_{\text{out}}$  is linked to  $\mathbf{V}_{\text{REF}}$ , by conception the difference between these two nodes for  $\mathbf{I}_{\mathsf{P}} = 0$  is maximum  $\pm$  25 mV, 2.475 V <  $\mathbf{V}_{\text{out}}$  < 2.525 V. <sup>3)</sup> In Ref<sub>out</sub> mode at  $\mathbf{T}_{\mathsf{A}} = 25^{\circ}\text{C}$ , 2.475 V <  $\mathbf{V}_{\text{REF}}$  < 2.525 V. The minimal impedance loading the ref pin should be > 220 k $\Omega$ . Internal impedance = 600  $\Omega$ . For most applications you need to buffer this output to feed it into an ADC for example. <sup>4)</sup> To overdrive the REF (1.9 V .. 2.7 V) max  $\pm$  1 mA is needed. <sup>5)</sup>  $\mathbf{I}_{\mathsf{C}} = \mathbf{I}_{\mathsf{P}}/\mathbf{N}_{\mathsf{C}}$ . <sup>6)</sup> Only due to TCR  $\mathbf{I}_{\mathsf{M}}$ .

#### **Features**

- Closed loop (compensated) multirange current transducer using the Hall effect
- Unipolar voltage supply
- Isolated plastic case recognized according to UL 94-V0
- Compact design for PCB mounting
- Incorporated measuring resistance
- · Extended measuring range
- Access to the internal voltage reference
- Possibility to feed the transducer reference from external supply.

#### **Advantages**

- Excellent accuracy
- Very good linearity
- Very low temperature drift
- Optimized response time
- Wide frequency bandwidth
- · No insertion losses
- High immunity to external interference
- · Current overload capability.

# **Applications**

- AC variable speed drives and servo motor drives
- Static converters for DC motor drives
- Battery supplied applications
- Uninterruptible Power Supplies (UPS)
- Switched Mode Power Supplies (SMPS)
- Power supplies for welding applications.

#### **Application Domain**

• Industrial.



#### **Current Transducer LTSR 6-NP**

C	General data		
$T_{_{A}}$	Ambient operating temperature	- 40 + 85	°C
T <sub>s</sub>	Ambient storage temperature	- 40 + 100	°C
Ü	Insulating material group	III a	
m	Mass	10	g
	Standards 1)	EN 50178: 1997	
		IEC 60950-1: 20	01

Isolation characteristic				
<b>V</b> <sub>d</sub> <b>v</b>	Rms voltage for AC isolation test, 50 Hz, 1 min	3	kV	
$\hat{\mathbf{V}}_{w}$	Impulse withstand voltage 1.2/50 μs	> 8	kV	
		Min		
$V_{\rm e}$	Rms voltage for partial discharge extinction 10 pC	> 1.5	kV	
		Min		
<b>d</b> Cp	Creepage distance 2)	15.35	mm	
<b>d</b> Cl	Clearance distance 3)	6.2	mm	
CTI	Comparative Tracking Index (group IIIa)	175		

# **Applications examples**

According to EN 50178 and CEI 61010-1 standards and following conditions:

- Over voltage category OV 3
- Pollution degree PD2
- Non-uniform field

	EN 50178	EIC 61010-1 Nominal voltage 600 V	
dCp, dCl, $\hat{\mathbf{V}}_{w}$	Rated insulation voltage	Nominal voltage	
Single insulation	600 V	600 V	
Reinforced insulation	300 V	300 V	

 $\underline{\text{Notes}}$ : <sup>1)</sup>Specification according to IEC 1000-4-8 not adhered to in DC, error according to two axes 1.5% instead of 1%

# **Safety**



This transducer must be used in electric/electronic equipment with respect to applicable standards and safety requirements in accordance with the manufacturer's operating instructions.



Caution, risk of electrical shock

When operating the transducer, certain parts of the module can carry hazardous voltage (eg. primary busbar, power supply).

Ignoring this warning can lead to injury and/or cause serious damage.

This transducer is a built-in device, whose conducting parts must be inaccessible after installation.

A protective housing or additional shield could be used.

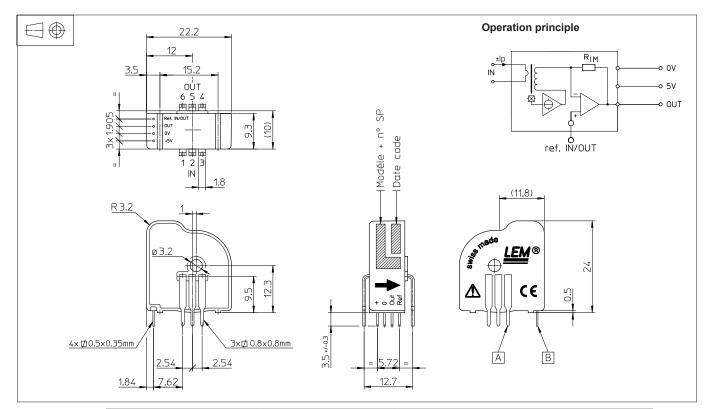
Main supply must be able to be disconnected.

<sup>&</sup>lt;sup>2)</sup>On housing

<sup>&</sup>lt;sup>3)</sup>On PCB with soldering pattern UTEC93-703.



# Dimensions LTSR 6-NP (in mm.)



Number of primary turns	Primary nominal current rms	Nominal <sup>1)</sup> output voltage V <sub>OUT</sub> [V]	Primary resistance <b>R</b> <sub>P</sub> [ mΩ ]	Primary insertion inductance L <sub>P</sub> [ µH ]	Recommended connections
1	± 6	2.5 ± 0.625	0.18	0.013	<u>6</u> _5_4_ OUT ○ ○ ○ ○ ○
2	± 3	2.5 ± 0.625	0.81	0.05	& 5 4 OUT ○ ○ ○ ○ ○ ○ ○ IN 1 2 3
3	± 2	2.5 ± 0.625	1.62	0.12	& 5 4 OUT 0 0 0

# **Mechanical characteristics**

General tolerance ± 0.2 mm
 Fastening & connection of primary 6 pins 0.8 x 0.8 mm

Recommended PCB hole 1.3 mm

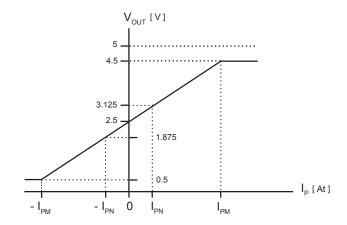
Fastening & connection of secondary A pins 0.5 x 0.35 mm Recommended PCB hole 0.8 mm
 Additional primary through-hole Ø 3.2 mm

#### **Remarks**

- V<sub>OUT</sub> swings above the 2.5 V offset when I<sub>P</sub> flows from terminals 1, 2, 3 to terminals 4, 5, 6 (with the arrow)
- For the EMC, the acceptance criteria are available on request
- Temperature of the primary conductor should not exceed 100°C.

<u>Note</u>: <sup>1)</sup>Output voltage when LTSR 6-NP is used with internal reference.

# **Output Voltage - Primary Current**



Page 3/3