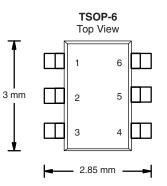


Marking Code:

Vishay Siliconix

N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)			
30	0.060 at V _{GS} = 10 V	4.5			
	0.085 at V _{GS} = 4.5 V	3.8			



Ordering Information: Si3454ADV-T1-E3 (Lead (Pb)-free)

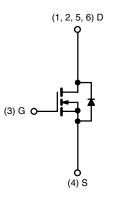
A4xxx

Si3454ADV-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_q Tested
- Compliant to RoHS Directive 2002/95/EC





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted Symbol Parameter 5 s **Steady State** Unit Drain-Source Voltage V_{DS} 30 V V_{GS} Gate-Source Voltage ± 20 $T_A = 25 \ ^{\circ}C$ 4.5 3.4 Continuous Drain Current (T_J = 150 °C)^a I_D T_A = 70 °C 3.6 2.7 А Pulsed Drain Current (10 µs Pulse Width) 20 I_{DM} 1.7 1.0 Continuous Source Current (Diode Conduction)^a I_S T_A = 25 °C 2.0 1.14 P_D w Maximum Power Dissipation^a T_A = 70 °C 1.3 0.73 Operating Junction and Storage Temperature Range T_J, T_{stg} - 55 to 150 °C

THERMAL RESISTANCE RATINGS								
Parameter		Symbol	Typical	Maximum	Unit			
	t ≤ 5 s	- R _{thJA}	50	62.5				
Maximum Junction-to-Ambient ^a	Steady State		90	110	°C/W			
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	30	36				

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.0		3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$	V _{GS} = 0 V		1		
		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 \text{ °C}$			25	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	15			А	
	R _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 4.5 \text{ A}$		0.048	0.060	Ω	
Drain-Source On-State Resistance ^a		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 3.8 \text{ A}$		0.070	0.085		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_{D} = 4.5 \text{ A}$		10		S	
Diode Forward Voltage ^a	V _{SD}	I _S = 1.7 A, V _{GS} = 0 V		0.8	1.2	V	
Dynamic ^b	- 1 1		•				
Total Gate Charge	Qg			9	15	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = 15 V, V_{GS} = 10 V, I_{D} = 4.5 A		2.5			
Gate-Drain Charge	Q _{gd}			1.5			
Gate Resistance	Rg		0.5		2.9	Ω	
Turn-On Delay Time	t _{d(on)}			10	20		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		10	20	ns	
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong \text{1}$ A, V_GEN = 10 V, R_g = 6 Ω		20	35		
Fall Time	t _f			7	15		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 1.7 A, dI/dt = 100 A/μs		40	80		

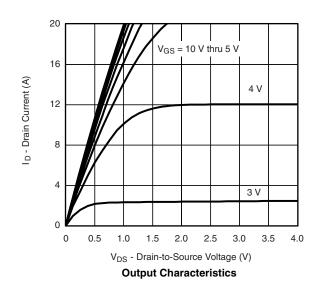
Notes:

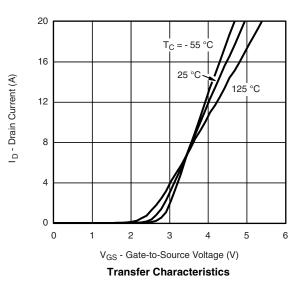
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



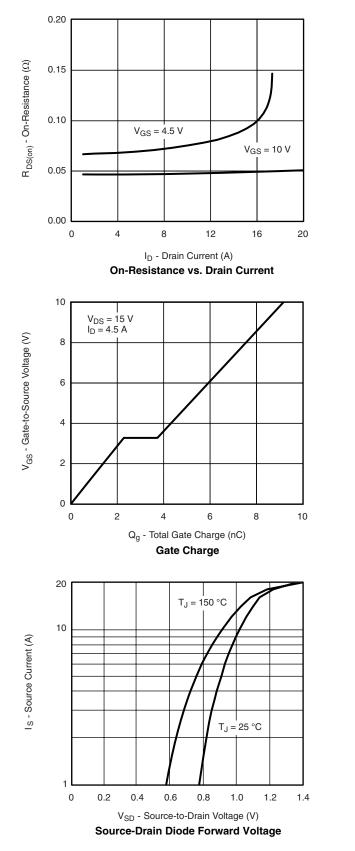


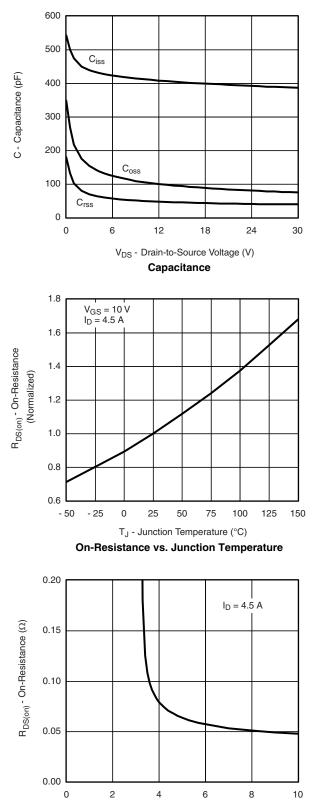


Si3454ADV

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





V_{GS} - Gate-to-Source Voltage (V) On-Resistance vs. Gate-to-Source Voltage

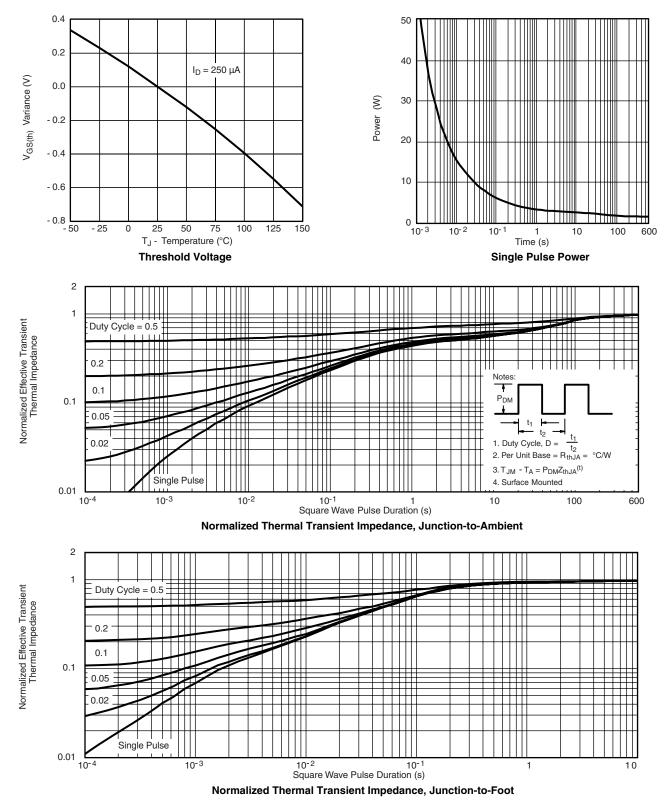
Document Number: 71108 S09-0765-Rev. D, 04-May-09

Si3454ADV



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71108.



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