

Ultra Low Jitter HCSL Crystal Oscillator 5.0 x 3.2mm

2.5/3.3V HCSL XO

NX504



5.0 x 3.2mm Ceramic SMD

Product Features

- Meet PCIe Gen2 and Gen3 clock requirements at 100MHz
- Very low phase jitter < 1.0ps RMS max.
- Wide frequency range $5 \sim 212.5 \text{MHz}$
- Thicker crystal for improved reliability
- Low supply current 70mA max.
- Industrial Temperature Range
- Pb-free & RoHS compliant
- Fast lead time

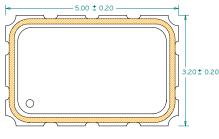
Product Description

The NX504 XO series is a high performance HCSL crystal oscillator family with very low jitter performance. Other than PCle clock frequencies, it also supports various options including other Networking frequencies, 2.5V/3.3V voltage, and various stabilities. It is designed to meet the clock source specifications for PCle interface, SGMII of communication systems, and other high performance equipment.

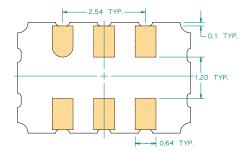
Applications

- Networking systems
- Servers and storage systems
- Profession video equipments
- Test and measurement
- FPGA/ASIC clock generation

Package: (Scale: none, Dimensions are in mm)







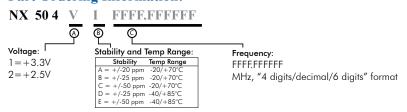
Recommended Land Pattern:
#6 = 2.54±0.10 = #4
1.6 TYP. ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓

Pin Functions:

Pin	Function						
1	OE Function						
2	N/C						
3	Ground						
4	Q						
5	\overline{Q}						
6	V _{CC}						

*Extended high frequency power decoupling is recommended (see test circuit for minimum recommendation). To ensure optimal performance, do not route RF traces beneath the package.

Part Ordering Information:



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Electrical Performance

Parameter		Min.	Тур.	Max.	Units	Notes	
Output Frequency		5		212.5	MHz		
Supply Voltage		3.135	3.3	3.465	17		
		2.375	2.5	2.625	V	See ordering options	
Supply Current, Out	tput Enabled			70	mA		
Supply Current, Output Disabled only				40	mA		
Frequency Stability				±50	ppm	See ordering options	
Operating Temperat	ture Range	-40		+85	°C	See ordering options	
Output Logic 0, VOI	L	-0.15	0		V		
Output Logic 1, V _{OH}		0.66	0.7	0.9	V		
Output Load		$R_S = 33\Omega$, $R_P = 50\Omega$, $C_L = 2pF$				Output requires termination	
Duty Cycle		45		55	%	Measured 50% V _{CC}	
Rise and Fall Time				700	ps	Measured from $V_{OL} = 0.175V$ to $V_{OH} = 0.5252V$	
Jitter, RMS	PCIe Gen2, 100 MHz		2.0	3.0	ps	As defined by PCI-SIG for PCIe Gen2	
Jitter, RMS	PCIe Gen3, 100 MHz		0.43	1.0	ps	As defined by PCI-SIG for PCIe Gen3	
Jitter, Accumulated , RMS (1-o)				6	ps	20.000 adjacent periods	
Jitter, Phase, RMS	<40MHz		0.4	1	ps	12kHz to 5 MHz frequency band	
	40 to 212.5MHz		0.4	1	ps	12kHz to 20 MHz frequency band	
	100MHz, 125MHz		0.4	0.6	ps	12kHz to 20 MHz frequency band	
Jitter, pk-pk				40	ps	100,000 random periods	

Notes:

1. Stability includes all combinations of operating temperature, load changes, rated input (supply) voltage changes, initial calibration tolerance (25°C),

aging (1 year at 25°C average effective ambient temperature), shock and vibration.

2. Phase jitter typical value is depending on output frequencies.

3. For specifications other than those listed, please contact sales.

Output Enable / Disable Function

Parameter	Min.	Тур.	Max.	Units	Notes
Input Voltage (pin 1), Output Enable	0.7 V _{CC}			V	or open
Input Voltage (pin 1), Output Disable (low power standby)			0.3 V _{CC}	V	Output is Hi-Z
Output Disable Delay			100	ns	
Output Enable Delay			100	ns	
Start up Time			10	ms	

Absolute Maximum Ratings

Parameter	Min.	Тур.	Max.	Units	Notes
Storage Temperature	-55		+125	°C	

For the latest product information visit: http://www.pericom.com/products/crystals-and-crystal-oscillators/hiflex-xo/?part=NX504

For test circuit go to: http://www.pericom.com/pdf/sre/tc-hcsl.pdf

For soldering reflow profile and reliability test ratings go to: <u>http://www.pericom.com/pdf/sre/reflow.pdf</u>

For tape and reel information go to: <u>http://www.pericom.com/pdf/sre/tr_5032_xo.pdf</u>

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