

5.7kVDC 6W Quad Output Isolated Gate Drive SM DC/DC Converters



FEATURES

- No opto feedback
- Patent protected
- Four isolated output voltages suitable for powering IGBT/SiC & Mosfet gate drives simultaneously in a three phase bridge configuration
- Reinforced insulation to UL60950 with 8mm creepage & clearance recognition pending
- ANSI/AAMI ES60601-1 recognition pending
- Characterised dv/dt immunity 80kV/µs at 1.6kV
- Characterised partial discharge performance
- 5.7kVDC isolation test voltage 'Hi Pot Test'
- Ultra low coupling capacitance typically 15pF
- DC link voltage 3kVDC
- 5V, 12V & 24V input voltages
- 105°C operating temperature

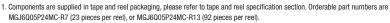
PRODUCT OVERVIEW

Offering four isolated output voltages of 24V, the MGJ6 series of DC-DC converters is ideal for powering 'high side' and 'low side' gate drive circuits simultaneously for IGBTs, Silicon and Silicon Carbide Mosfets in three phase circuits.The MGJ6 series is characterised for high isolation and dv/dt requirements commonly seen in bridge circuits used in motor drives and inverters.

SELECTION GUIDE								
		Output 1 VHA / Output 2 VHB Output 3 VHC			Output 4 VL			
Order Code ¹	Input Voltage Range	Rated Output Voltage	Rated Output Current	Output Power	Rated Output Voltage	Rated Output Current	Output Power	
	V	V	mA	W	V	mA	W	
MGJ6Q05P24MC	4.5 - 9	24						
MGJ6Q12P24MC	9 - 18	24	42	1	24	125	3	
MGJ6Q24P24MC	18 - 36	24	42	1	24	125	3	

SELECTION GUIDE	(Continued))							
		Out	put 1 VHA / Output 3		НВ		Out	put 4 VL	
Order Code ¹	Input Voltage Range	Load Regulation (Typ)	Load Regulation (Max)	Ripple & Noise (Typ) ²	Ripple & Noise (Max) ²	Load Regulation (Typ)	Load Regulation (Max)	Ripple & Noise (Typ) ²	Ripple & Noise (Max) ²
	V		%	mV	р-р	9	6	mV	р-р
MGJ6Q05P24MC	4.5 - 9								
MGJ6Q12P24MC	9 - 18	2	3	70	120	2	3	70	120
MGJ6Q24P24MC	18 - 36	2	3	70	120	2	3	70	120

SELECTION GUIDE (Continued)							
	Ħ	nt ad			MT	TF ³	
Order Code ¹	Nominal Input Voltage	Input Current at Rated Load	Efficiency (Min)	Efficiency (Typ)	MIL 217	Telecordia	
	V	mA	9	6	kH	Irs	
MGJ6Q05P24MC	5						
MGJ6Q12P24MC	12	600	81	84			
MGJ6Q24P24MC	24	300	82	85			



2. See ripple & noise test method.

3. Calculated using MIL-HDBK-217 FN2 and Telecordia SR-332 calculation model at TA=25°C with nominal input voltage at full load.

All specifications typical at TA=25°C, nominal input voltage and rated output current unless otherwise specified.







muRata P. Murata Power Solution

MGJ6 Three Phase Bridge Series

INPUT CHARACTERISTICS	On Altions	Min	T	Man	Haita
Parameter	Conditions	Min.	Typ.	Max.	Units
	5V input types	4.5	5 12	9 18	
Voltage range		12V input types 9			V
	24V input types	18	24	36	
	Turn on threshold MGJ6Q05		4.1		
	Turn off threshold MGJ6Q05		3.0		
Index velte as leak out	Turn on threshold MGJ6Q12		8.1		V
Jnder voltage lock out	Turn off threshold MGJ6Q12		7.5		V
	Turn on threshold MGJ6Q24		16.7		
	Turn off threshold MGJ6Q24		16.3		
Input ripple current	5V input types				
	12V input types		45		mA
	24V input types		25		p-p
OUTDUT OUADAOTEDIOTIOS	1 3				
OUTPUT CHARACTERISTICS Parameter	Conditions	Min.	Typ.	Max.	Units
Minimum load	Below 10% load, output may rise to 30V maximum voltage	10	īyρ.	WICK.	%
/oltage set point accuracy	All output types		+3 / -2		%
Total regulation	The output types		.07 =	10	%
Line regulation	Low line to high line		0.5	1	%
ISOLATION CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
arameter	Flash tested for 1 second (input to output)	4000	īyp.	iviax.	Ullita
	Flash tested for 1 second (output to output)	2500			VAC
solation test voltage	Qualification tested for 1 minute (input to output)	5700			
	Qualification tested for 1 minute (output to output)	3000			VDC
Resistance	Viso = 1kVDC	100			GΩ
Continuous barrier withstand voltage	Non-safety barrier application	100		3000	VDC
				8	
· ·	Input to output				
· ·	Input to output				mm
· ·	Output to output		15	8	mm
Creepage & clearance			15 15		mm pF

	Primary to Output 3 vnC		15		
	Primary to Output 4 VL		15		
GENERAL CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Power Consumption	Disable pin pulled low		45		mW
Switching frequency			100		kHz

TEMPERATURE CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Operation	See derating graphs	-40		105	
Storage		-55		125	°C
Product temperature rise above ambient	100% Load, Nom V _{IN} , Still Air		19		

ABSOLUTE MAXIMUM RATINGS	
Short-circuit protection	Continuous
Input voltage, MGJ6 5V input types	12V
Input voltage, MGJ6 12V input types	20V
Input voltage, MGJ6 24V input types	40V



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TECHNICAL NOTES

ISOLATION VOLTAGE

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions MGJ6 series of DC/DC converters are all 100% production tested at 4kVACrms for 1 second from input to output and 2.5kVACrms for 1 second from output to output. Also they are all qualification tested at 5.7kVDC for 1 minute from input to output and 3kVDC for 1 minute from output.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

When the insulation in the MGJ6 series is not used as a safety barrier, i.e. provides functional isolation only, continuous or switched voltages across the barrier up to 3kV are sustainable. Long term reliability testing at these voltages continues. Peak Inception voltages measured were in excess of 3.5kV when testing for partial discharge in accordance with IEC 60270. Please contact Murata for further information.

The MGJ6 series pending recognition by Underwiters Laboratory to 250 Vrms Reinforced Insulation, please see safety approval section below.

REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

SAFETY APPROVAL

ANSI/AAMI ES60601-1

The MGJ6 series is pending recognition ANSI/AAMI ES60601-1.

UL 60950

The MGJ6 series pending recognition by Underwriters Laboratory (UL) to UL 60950 for reinforced insulation to a working voltage of 250Vrms with a maximum measured product operating temperature of 105°C.

Creepage and clearance 8mm, input to output & across outputs.

FUSING

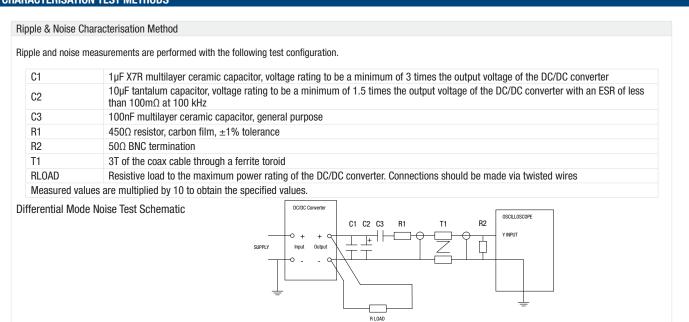
The MGJ6 Series of converters are not internally fused so to meet the requirements of UL an anti-surge input line fuse should always be used with ratings as defined below. Input Voltage, 5V 4A

Input Voltage, 12V 2A

Input Voltage, 24V 1A

All fuses should be UL recognized, 250Vac rated.

CHARACTERISATION TEST METHODS

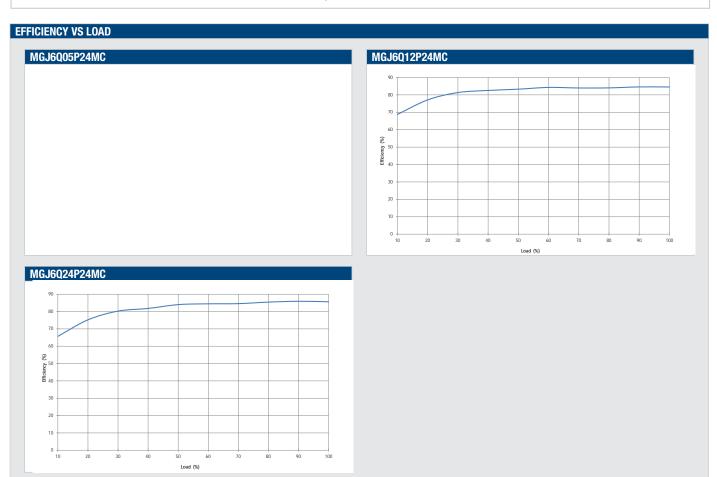


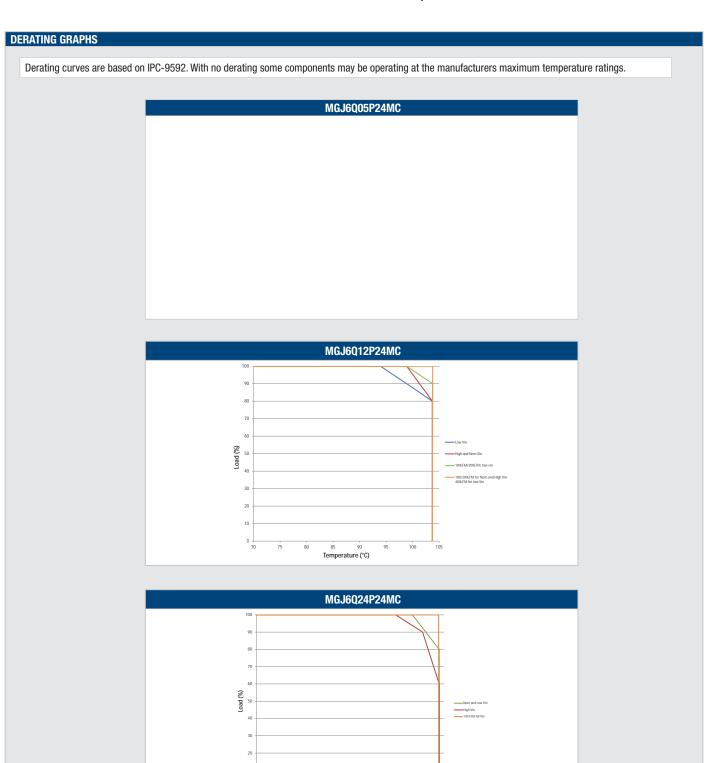
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ROHS COMPLIANCE, MSL AND PSL INFORMATION



This series is compatible with Pb-Free soldering systems and is also backward compatible with Sn/Pb soldering systems. The MGJ6 three phase series has a process, moisture, and reflow sensitivity classification of MSL2 PSL R7F as defined in J-STD-020 and J-STD-075. This translates to: MSL2 = 1 year floor life, PSL R7F = Peak reflow temperature 245°C with a limitation on the time above liquidus (217°C) which for this series is 90sec max. The pin termination finish on this product series is Gold with Nickel Pre-plate.



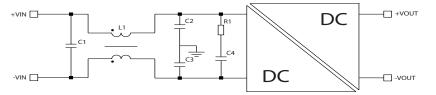


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EMC FILTERING AND SPECTRA

FILTERING

The following filter circuit and filter table shows the input filters typically required to meet EN 55022 Curve B, Quasi-Peak EMC limit, as shown in the following plots. The following plots show positive and negative quasi peak and CISPR22 Average Limit B (purple line) and Quasi Peak Limit B (pink line) adherence limits.

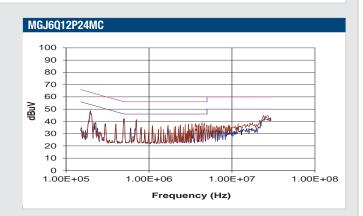


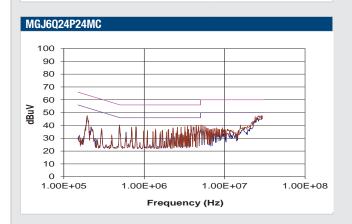
C1, C2 & C3 Polyester or ceramic capacitor

Electrolytic capacitor (note R1 could be omitted if C4 has ESR >= R1)

TO MEET CURVE B							
Part Number	C1	L1	Part Number	C2	C3	R1	C4
MGJ6Q05P24MC							
MGJ6Q12P24MC	10μF	1mH	51105C	1nF	1nF	1Ω	470µF
MGJ6Q24P24MC	10μF	1mH	51105C	1nF	1nF	1Ω	470µF

MGJ6Q05P24MC







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APPLICATION NOTES

Start-up times

Typical start up times for this series, with recommended maximum additional output capacitance are:

Part No.	Start-up times
i ait ivo.	ms
MGJ6Q05P24MC	30
MGJ6Q12P24MC	30
MGJ6Q24P24MC	30

Output capacitance must not exceed:

Output Voltage	Maximum output capacitance
V	μF
24 VHA	18
24 VHB	18
24 VHC	18
24 VL	56

Disable/Frequency synchronisation

Please refer to application notes for further information.

		Min	Тур	Max	Units
Disable/Synch (Pin is active low)	Pull Down Current		0.5		mA
	Input High	2		5	V
(i iii is active low)	Input Low	0		0.8	V
Cumphropioation	Frequency Range	90	100	110	kHz
Synchronisation	Duty Cycle	25		75	%

The Disable/Synchronization pin has three modes:

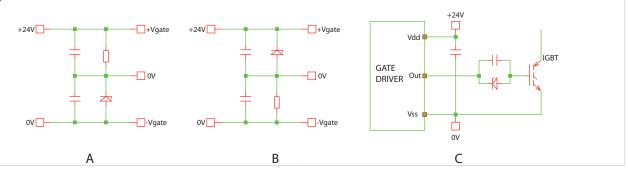
- 1. When a dc logic low voltage is applied to this pin the MGJ6 is disabled and enters a low guiescent current sleep mode.
- 2. When this pin is left floating or a dc logic high (CMOS/TTL compatible) voltage is applied the MGJ6 is enabled and operates at the programmed frequency of 100kHz.
- 3. When a square wave of between 90kHz and 110kHz is applied to this pin, the switcher operates at the same frequency as the square wave. The falling edge of the square wave corresponds to the start of the switching cycle. If the signal is slower than 25Hz, it will be interpreted as enabling and disabling the part. If the MGJ6 is disabled, it must be disabled for 7 clock cycles before being re-enabled.

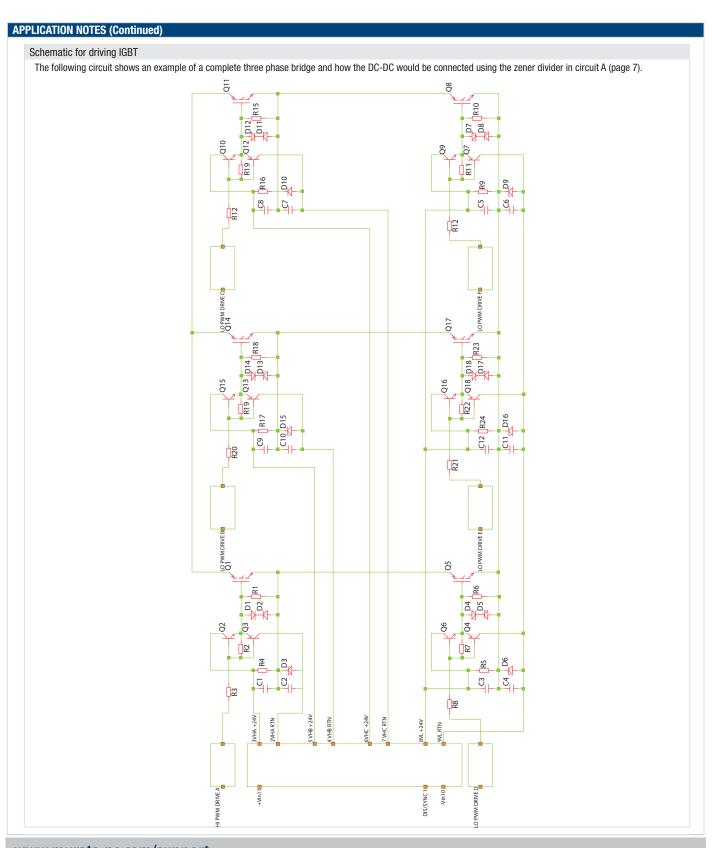
Output configurations for power switches

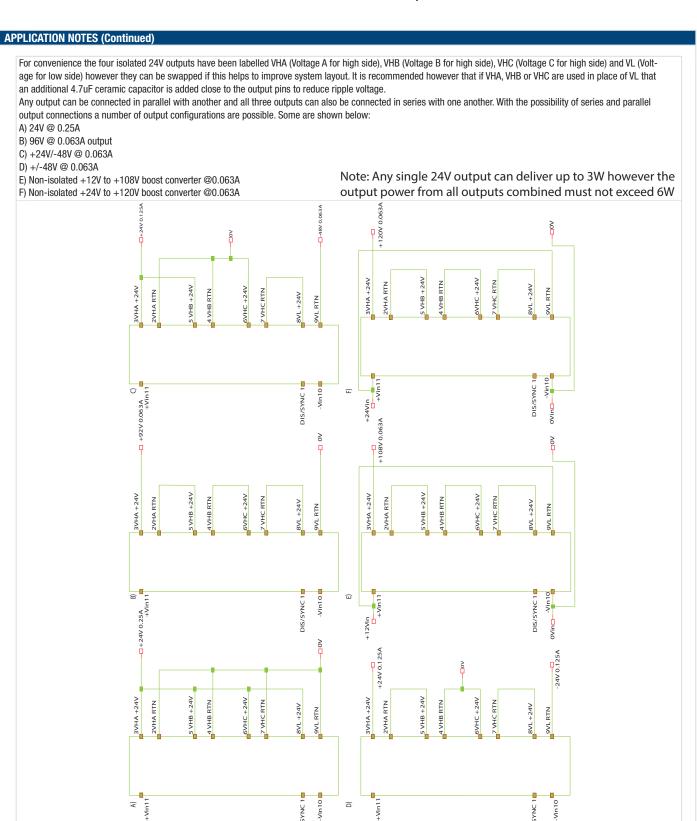
There are several zener based divider circuits that can be used to configure a bipolar output for gate drives as shown below. The table below shows suggested component values for various power switches using circuit A.

Component	IGBT	SIC	MOSFET
Zener diode ¹	9V1	5V1	9V1
Resistor	15K	18K	15K

1. Suggested zener diode is BZX84C.









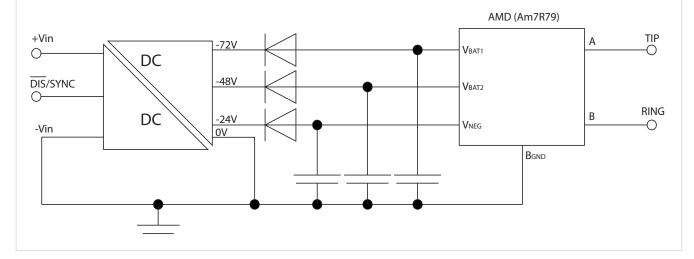
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APPLICATION NOTES (Continued)

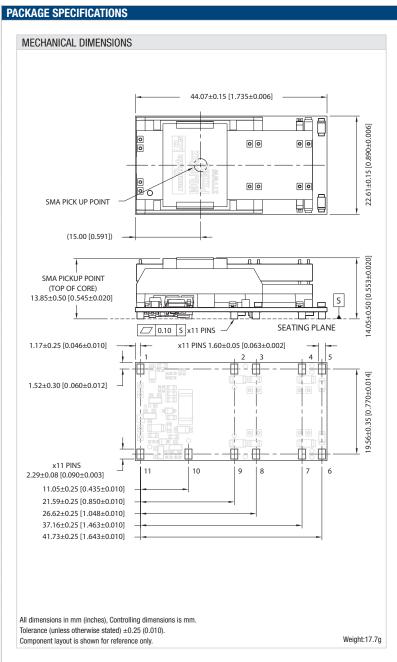
SLIC Circuits

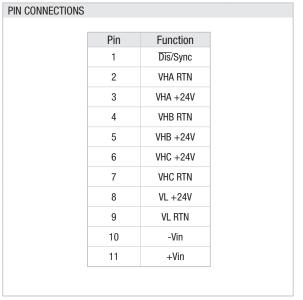
Power source is preferred to the telephone system power due to either the power quality of the telecommunications system power supply or to avoid potential power line disturbances, such as lightning strikes and access switching, which will affect the target circuit function.

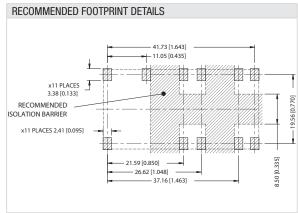
Another application area is in fibre-in-the-loop (FITL) or radio-in-the-loop (RITL) interfacing via a standard telecommunication SLIC, where the usual telecommunication battery voltage is not available due to the transmission media in use (fibre or radio). In particular, FITL/RITL interfaces directly on PC cards, in local monitor and boost circuits and at exchanges between the fibre/radio and wire media. The supply rails can be used for ringing generators as well as SLIC circuits or where both are combined, such as in the AMD AM79R79 Ringing SLIC device (see figure 2). The -72V rail is used primarily for the generation of the ringing signal (VBAT1), the -48V rail is used to supply in line access circuitry (VBAT2) and the -24V supply for the on-chip regulator for the logic interface (VNEG). Alternative devices from other manufacturers could use the \pm -24V outputs for their internal circuit supply and \pm 72V for ringing.







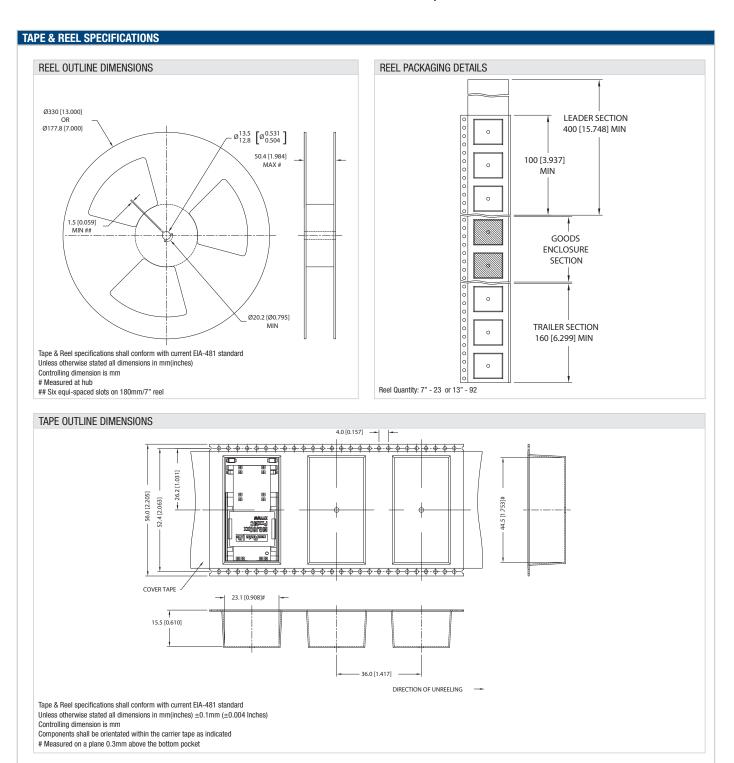






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