### ES2A-M3, ES2B-M3, ES2C-M3, ES2D-M3

Vishay General Semiconductor

COMPLIANT

HALOGEN

**FREE** 

### **Surface Mount Ultrafast Plastic Rectifier**



DO-214AA (SMB)

PRIMARY CHARACTERISTICS							
I <sub>F(AV)</sub>	2.0 A						
$V_{RRM}$	50 V, 100 V, 150 V, 200 V						
I <sub>FSM</sub>	50 A						
t <sub>rr</sub>	20 ns						
V <sub>F</sub>	0.90 V						
T <sub>J</sub> max.	150 °C						
Package	DO-214AA (SMB)						
Diode variations	ions Single die						

#### **FEATURES**

- Glass passivated pellet chip junction
- · Ideal for automated placement
- Ultrafast recovery times for high efficiency
- Low forward voltage, low power losses
- · High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  -Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### TYPICAL APPLICATIONS

For use in high frequency rectification and freewheeling application in switching mode converters and inverters for consumer, computer, and telecommunication.

#### **MECHANICAL DATA**

Case: DO-214AA (SMB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - RoHS-compliant, commercial grade Base P/NHM3 - RoHS-compliant, AEC-Q101 qualified

**Terminals:** Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test **Polarity:** Color band denotes cathode end

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	ES2A	ES2B	ES2C	ES2D	UNIT	
Device marking code		EA	EB	EC	ED		
Maximum repetitive peak reverse voltage	$V_{RRM}$	50	100	150	200	V	
Maximum RMS voltage	$V_{RMS}$	35	70	105	140	V	
Maximum DC blocking voltage	$V_{DC}$	50 100 150		150	200	V	
Maximum average forward rectified current at $T_L = 110  ^{\circ}\text{C}$	I <sub>F(AV)</sub>	2.0					
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	50					
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150				°C	



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	ES2A	ES2B	ES2C	ES2D	UNIT
Maximum instantaneous forward voltage	2.0 A		V <sub>F</sub> <sup>(1)</sup>	0.90			V	
Maximum DC reverse current at rated	n DC reverse current at rated $T_A = 25 ^{\circ}\text{C}$			10				
DC blocking voltage		T <sub>A</sub> = 100 °C	l <sub>R</sub>	350			μA	
Max. reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$	t <sub>rr</sub>		20			ns	
Manipulation	$I_F = 2.0 \text{ A}, V_R = 30 \text{ V},$ $dI/dt = 50 \text{ A/}\mu\text{s},$ $I_r = 10 \% I_{RM}$	T <sub>J</sub> = 25 °C		30				
Maximum reverse recovery time		T <sub>J</sub> = 100 °C	t <sub>rr</sub>		5	0	ns	
Maximum stored charge	$I_F = 2.0 \text{ A}, V_R = 30 \text{ V},$ $dI/dt = 50 \text{ A/}\mu\text{s},$ $I_r = 10 \% I_{RM}$	T <sub>J</sub> = 25 °C	0	10			. 0	
		T <sub>J</sub> = 100 °C	Q <sub>rr</sub>		2	25		nC
Typical junction capacitance	4.0 V, 1 MHz		CJ		1	8		pF

#### Note

 $<sup>^{(1)}</sup>$  Pulse test: 300 ms pulse width, 1 % duty cycle

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	DL ES2A ES2B ES2C ES2D				UNIT
Typical thermal registance	R <sub>0JA</sub> (1)			°C/W		
Typical thermal resistance			C/VV			

#### Note

(1) Units mounted on PCB 5.0 mm x 5.0 mm (0.013 mm thick) land areas

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
ES2D-M3/52T	0.096	52T	750	7" diameter plastic tape and reel			
ES2D-M3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel			
ES2DHM3/52T (1)	0.096	52T	750	7" diameter plastic tape and reel			
ES2DHM3/5BT (1)	0.096	5BT	3200	13" diameter plastic tape and reel			

#### Note

(1) AEC-Q101 qualified

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### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

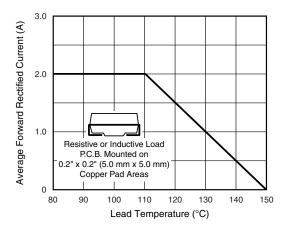


Fig. 1 - Maximum Forward Current Derating Curve

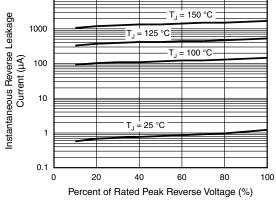


Fig. 4 - Typical Reverse Leakage Characteristics

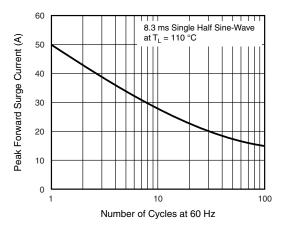


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

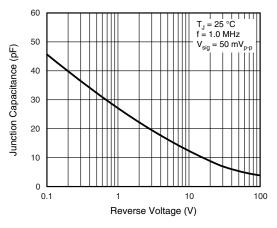


Fig. 5 - Typical Junction Capacitance

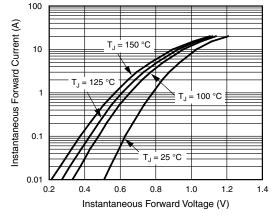


Fig. 3 - Typical Instantaneous Forward Characteristics

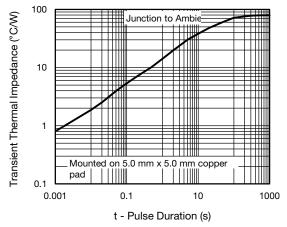


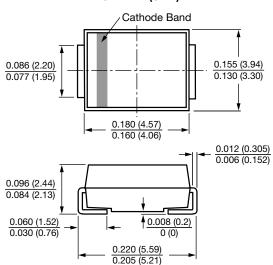
Fig. 6 - Typical Transient Thermal Impedance

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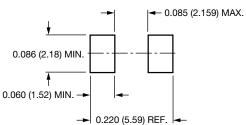
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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

#### **DO-214AA (SMB)**



# Mounting Pad Layout





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