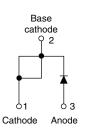


VS-MBR7...PbF Series, VS-MBR7...-N3 Series

Vishay Semiconductors

Schottky Rectifier, 7.5 A





τо	-220	AC

PRODUCT SUMMARY						
Package	TO-220AC					
I _{F(AV)}	7.5 A					
V _R	35 V, 45 V					
V _F at I _F	0.57 V					
I _{RM} max.	15 mA at 125 °C					
T _J max.	150 °C					
Diode variation	Single die					
E _{AS}	7 mJ					

FEATURES

- 150 °C T_J operation
- High frequency operation
- Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



- RoHS COMPLIANT HALOGEN
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

The VS-MBR7... Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES UNIT							
I _{F(AV)}	Rectangular waveform	7.5	A						
V _{RRM}		35/45	V						
I _{FSM}	t _p = 5 μs sine	690	A						
V _F	7.5 A _{pk} , T _J = 125 °C	0.57	V						
TJ	Range	- 65 to 150	°C						

VOLTAGE RATINGS										
PARAMETER	SYMBOL	VS-MBR735PbF	VS-MBR735-N3	VS-MBR745PbF	VS-MBR745-N3	UNITS				
Maximum DC reverse voltage	VR					v				
Maximum working peak reverse voltage	V _{RWM}	35	35	45	45					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CON	DITIONS	VALUES	UNITS				
Maximum average forward current	I _{F(AV)}	T _C = 131 °C, rated V _R	$T_{C} = 131 \text{ °C}$, rated V_{R}						
Non-repetitive peak surge current	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	690	A				
		Surge applied at rated load condition half wave single phase 60 Hz		150					
Non-repetitive avalanche energy	E _{AS}	$T_{J} = 25 \text{ °C}, I_{AS} = 2 \text{ A}, L = 3.5 \text{ r}$	7	mJ					
Repetitive avalanche current	I _{AR}	Current decaying linearly to ze Frequency limited by T _J maxir	2	А					

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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS						
		15 A	T _J = 25 °C	0.84					
Maximum forward voltage drop	V _{FM} ⁽¹⁾	7.5 A	T.I = 125 °C	0.57	V				
		15 A	1j = 125 C	0.72					
Maximum instantaneous reverse current	I _{RM} ⁽¹⁾	T _J = 25 °C	Rated DC voltage	0.1					
Maximum instantaneous reverse current		T _J = 125 °C	haled DC vollage	15	mA				
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal rang	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C						
Typical series inductance	L _S	Measured from top of term	8.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R		1000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction temperature range	TJ		- 65 to 150	ů					
Maximum storage temperature range	T _{Stg}		- 65 to 175	C					
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	3.0	°C/W					
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50	0/00					
Approximate weight			2	g					
Approximate weight			0.07	oz.					
Mounting torque minimum			6 (5)	kgf ⋅ cm					
maximum			12 (10)	(lbf ⋅ in)					
Marking device		Case style TO-220AC	MBR735						
			MBR745						



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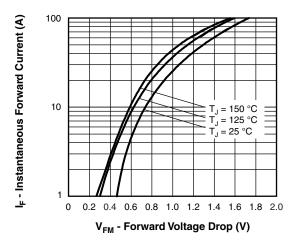


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

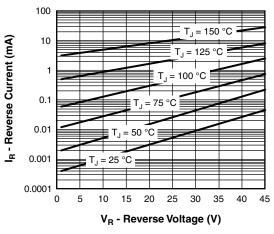


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

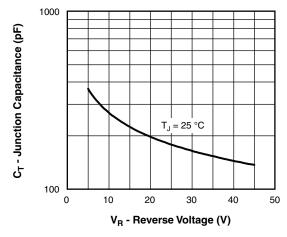
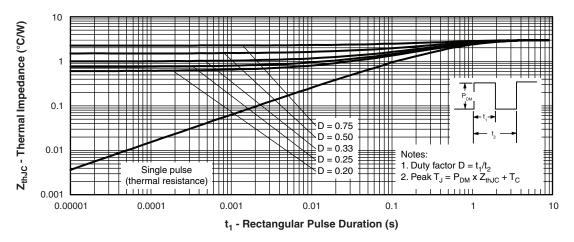


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





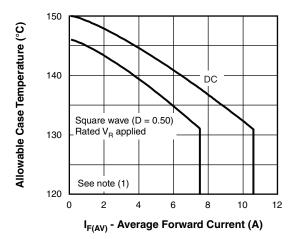
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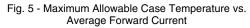
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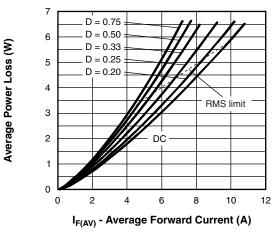


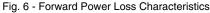
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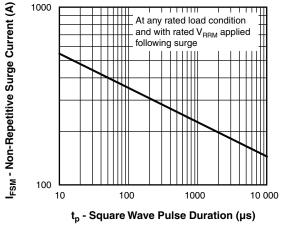


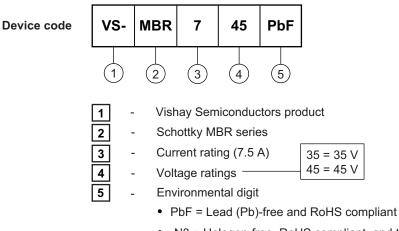
Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note



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ORDERING INFORMATION TABLE



• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)										
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-MBR735PbF	50	1000	Antistatic plastic tube							
VS-MBR735-N3	50	1000	Antistatic plastic tube							
VS-MBR745PbF	50	1000	Antistatic plastic tube							
VS-MBR745-N3	50	1000	Antistatic plastic tube							

LINKS TO RELATED DOCUMENTS						
Dimensions		www.vishay.com/doc?95221				
	TO-220AC PbF	www.vishay.com/doc?95224				
Part marking information	TO-220AC -N3	www.vishay.com/doc?95068				
SPICE model		www.vishay.com/doc?95298				



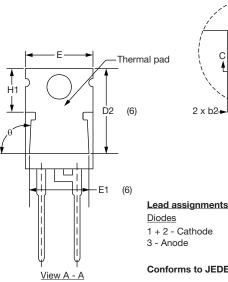
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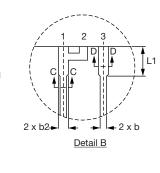
TO-220AC

plane

DIMENSIONS in millimeters and inches









Diodes 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220AC

SYMBOL	MILLIM	IETERS	INC	HES	NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBUL	MIN.	MAX.	MIN.	MAX.	NOTES	OTIMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.25	4.65	0.167	0.183			E1	6.86	8.89	0.270	0.350	6
A1	1.14	1.40	0.045	0.055			E2	-	0.76	-	0.030	7
A2	2.56	2.92	0.101	0.115			е	2.41	2.67	0.095	0.105	
b	0.69	1.01	0.027	0.040			e1	4.88	5.28	0.192	0.208	
b1	0.38	0.97	0.015	0.038	4		H1	6.09	6.48	0.240	0.255	6, 7
b2	1.20	1.73	0.047	0.068			L	13.52	14.02	0.532	0.552	
b3	1.14	1.73	0.045	0.068	4		L1	3.32	3.82	0.131	0.150	2
с	0.36	0.61	0.014	0.024			L3	1.78	2.13	0.070	0.084	
c1	0.36	0.56	0.014	0.022	4		L4	0.76	1.27	0.030	0.050	2
D	14.85	15.25	0.585	0.600	3		ØР	3.54	3.73	0.139	0.147	
D1	8.38	9.02	0.330	0.355			Q	2.60	3.00	0.102	0.118	
D2	11.68	12.88	0.460	0.507	6		θ	90° t	o 93°	90° t	o 93°	
E	10.11	10.51	0.398	0.414	3, 6							

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- ⁽⁴⁾ Dimension b1, b3 and c1 apply to base metal only
- ⁽⁵⁾ Controlling dimension: inches
- ⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2 and E1
- ⁽⁷⁾ Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- ⁽⁸⁾ Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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